

Form PTO-1449 (modified)

SEP 20 2001

List of Patents and Publications for Applicant's

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Atty. Docket No.

SILA:073

Serial No.

09/821,340

Applicants

G. DIWAKAR VISHAKHADATTA ET AL.

Filing Date:

3/29/01

Group:

2637

U.S. Patent Documents

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Foreign Patent Documents

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U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
✓	A1	5,828,955	10/27/98	Lipowski et al.	_____	_____	8/30/95
✓	A2	6,035,186	3/7/00	Moore et al.	_____	_____	3/11/97
✓	A3	6,075,979	6/13/00	Holtvoeth et al.	_____	_____	3/5/97
✓	A4	5,764,171	6/9/98	Stikvoort	_____	_____	4/2/96

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
✓	B1	WO 00/22735	4/20/00	Ali	_____	_____	
✓	B2	GB2233518A	1/9/91	Dedic	_____	_____	
✓	B3	0643477A2	3/15/95	Hulkko et al.	_____	_____	
✓	B4	WO 00/11794	3/2/00	Moore et al.	_____	_____	
✓	B5	WO 00/01074	1/6/00	Van Der Zwan et al.	_____	_____	

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
✓	C1	Stephen Jantzi et al., "Quadrature Bandpass $\Delta\Sigma$ Modulation for Digital Radio," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 1935-1950.
✓	C2	Stephen Jantzi et al., "A Complex Bandpass $\Delta\Sigma$ Converter For Digital Radio," ISCAS, May/June 1994, pp. 453-456.
✓	C3	"Analog Devices Delivers World's First Open Market GSM Direct Conversion Radio Chipset," Analog Devices Corporate Information Press Release, http://contentanalog.com/pressrelease/prdisplay/0,1622,102,00.html , September 13, 1999, pp. 1-4.

Examiner:

Date Considered:

11/7/05

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Exam. Init.	Ref. Des.	Citation
21	C4	Data Sheet, CX74017, "RF Transceiver for Single, Dual, or Tri-Band GSM/GPRS Applications," Conexant, January 2, 2001, pp. 1-16.
	C5	Jacques C. Rudell et al, "A 1.9-GHz Wide-Band IF Double Conversion CMOS Receiver for Cordless Telephone Applications," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 2071-2088.
	C6	Jan Crols et al., "Low-IF Topologies for High-Performance Analog Front Ends of Fully Integrated Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pp. 269-282.
	C7	Jacques C. Rudell et al., "Recent Developments In High Integration Multi-Standard CMOS Transceiver for Personal Communication Systems," invited paper at the 1998 International Symposium on Low Power Electronics, Monterey, California, 6 pgs.
	C8	Asad Abidi, "CMOS Wireless Transceivers: The New Wave," IEEE Communications Magazine, August 1999, pp. 119-124.
	C9	Data Sheet, UAA3535HL, "Low Power GSM/DCS/PCS Multi-band Transceiver," Philips Semiconductors, February 17, 2000, pp. 1-24.
	C10	Stephen Jantzi et al., "FP 13.5: A Quadrature Bandpass $\Delta\Sigma$ Modulator for Digital Radio," Digest of Technical Papers, 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, pp. 216-217, 460.
	C11	S. A. Jantzi et al., "The Effects of Mismatch In Complex Bandpass $\Delta\Sigma$ Modulators," IEEE, 1996, pp. 227-230.
	C12	Qiuting Huang, "CMOS RF Design-The Low Power Dimension," IEEE 2000 Custom Integrated Circuits Conference, pp. 161-166.
	C13	Paolo Orsatti et al., "A 20-mA-Receive, 55-mA-Transmit, Single-Chip GSM Transceiver in 0.25- μ m CMOS," IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, pp. 1869-1880.
	C14	Qiuting Huang et al., "The Impact of Scaling Down to Deep Submicron on CMOS RF Circuits," IEEE Journal of Solid-State Circuits, Vol. 33, No. 7, July 1998, pp. 1023-1036.
21	C15	Behzad Razavi, "Design Considerations for Direct-Conversion Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 44, No. 6, June 1997, pp. 428-435.

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Exam. Init.	Ref. Des.	Citation
71	C16	Farbod Behbahani et al., "CMOS Mixers and Polyphase Filters for Large Image Rejection," IEEE Journal of Solid-State Circuits, Vol. 36, No. 6, June 2001, pp. 873-887.
71	C17	Jan Crols et al., "A Single-Chip 900 MHz CMOS Receiver Front-End With A High Performance Low-IF Topolgy," IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, pp. 1483-1492.
71	C18	Analog Devices, Single-Chip Direct-Conversion GSM/GPRS/EDGE RFIC, Othello One, www.analog.com , 2 pgs.
71	C19	Analog Devices, AD6523/AD6524, GSM Direct Conversion Radio Chip Set, www.analog.com , 2 pgs.
71	C20	Analog Devices, GSM 3 V Transceiver IF Subsystem, AD6432, www.analog.com , pp. 1-20.

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26317U.S. Patent Documents
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See Pages 3-8**U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
✓	A5	6,148,048	11/14/00	Kerth et al.	—	—	9/26/97
	A6	4,713,563	12/15/87	Marshall et al.	—	—	5/12/86
	A7	4,070,632	1/24/78	Tuttle	—	—	9/22/76
	A8	4,236,252	11/25/80	Kominami et al.	—	—	2/6/79
	A9	4,680,588	7/14/87	Cantwell	—	—	12/5/85
	A10	4,857,928	8/15/89	Gailus et al.	—	—	1/28/88
	A11	4,989,074	1/29/91	Matsumoto	—	—	9/21/89
	A12	5,050,192	9/17/91	Nawata	—	—	11/21/90
	A13	5,083,304	1/21/92	Cahill	—	—	9/28/90
	A14	5,142,695	8/25/92	Roberts et al.	—	—	3/21/91
	A15	5,194,826	3/16/93	Huusko	—	—	4/12/91
	A16	5,235,410	8/10/93	Hurley	—	—	7/10/91
	A17	5,267,272	11/30/93	Cai et al.	—	—	2/14/91
	A18	5,283,578	2/1/94	Ribner et al.	—	—	11/16/92
	A19	5,345,406	9/6/94	Williams	—	—	8/25/92
	A20	5,430,890	7/4/95	Vogt et al.	—	—	11/20/92
	A21	5,442,353	8/15/95	Jackson	—	—	10/25/93
	A22	5,451,948	9/19/95	Jekel	—	—	2/28/94
	A23	5,500,645	3/19/96	Ribner et al.	—	—	3/14/94
	A24	5,557,642	9/17/96	Williams	—	—	11/14/94
	A25	5,712,628	1/27/98	Phillips et al.	—	—	8/31/95
	A26	5,742,189	4/21/98	Yoshida et al.	—	—	9/14/95
✓	A27	5,862,465	1/19/99	Ou	—	—	12/30/96

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Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
✓	A28	5,973,601	10/26/99	Campana	✓	✓	12/2/97
	A29	5,758,276	5/26/98	Shirakawa et al.	✓	✓	5/31/96
	A30	5,740,524	4/14/98	Pace et al.	✓	✓	12/14/95
	A31	4,623,926	11/18/86	Sakamoto	✓	✓	11/9/836
	A32	5,341,135	8/23/94	Pearce	✓	✓	4/30/92
	A33	5,241,310	8/31/93	Tiemann	✓	✓	3/2/92
	A34	4,562,591	12/31/85	Stikvoort	✓	✓	2/2/84
	A35	5,243,345	2/21/92	Naus et al.	✓	✓	2/21/92
	A36	5,469,475	11/21/95	Voorman	✓	✓	5/31/91
	A37	4,912,729	3/27/90	Van Rens et al.	✓	✓	12/15/88
	A38	4,627,021	12/2/86	Persoon et al.	✓	✓	3/13/84
	A39	4,692,737	9/8/87	Stikvoort et al.	✓	✓	10/17/86
	A40	4,584,659	4/22/86	Stikvoort	✓	✓	7/5/83
	A41	4,797,845	1/10/89	Stikvoort	✓	✓	12/11/86
	A42	4,604,720	8/5/86	Stikvoort	✓	✓	3/16/84
	A43	5,157,343	10/20/92	Voorman	✓	✓	5/31/91
	A44	5,124,705	7/23/92	Voorman	✓	✓	7/10/91
	A45	4,468,790	8/28/84	Hofelt	✓	✓	2/16/82
	A46	5,859,878	1/12/99	Phillips et al.	✓	✓	8/31/95
	A47	6,323,735	11/27/01	Welland et al.	✓	✓	5/25/00
✓	A48	6,167,245	12/26/00	Welland	✓	✓	5/29/98

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Foreign Patent Documents

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7	B6	WO 99/22456	5/6/99	Grenabo			10/27/98

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

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Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
✓	C91	Search Report for PCT/US02/00895; November 7, 2002; 6 pgs.

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21	A49	6,539,066	3/25/03	Heinen			11/10/99
21	A50	6,343,207	1/29/02	Hessel et al.			11/3/98
21	A51	6,002,925	12/14/99	Vu et al.			3/24/97

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